

RESPONSE UNDER 37 C.F.R. 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2188

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the Application of:

Serge Lasserre (TI-31354)

Conf. No.: 4434

Serial No: 09/932,381

Examiner: Reginald G. Bragdon

Filed: August 17, 2001

Art Unit: 2188

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

AMENDMENT UNDER RULE 116

Introductory Comments:

This paper is presented in response to the Office Action mailed on October 7, 2004. Entry of this amendment in this application, and reconsideration of the final rejection of claims in this application, are respectfully requested.

Proposed amendment to the claims begin on page 2.

Applicant's remarks begin on page 8.

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